

Abstracts

Dual-Diode Limiter for High-Power/Low-Spike-Leakage Applications

R.J. Tan and R. Kaul. "Dual-Diode Limiter for High-Power/Low-Spike-Leakage Applications." 1990 MTT-S International Microwave Symposium Digest 90.2 (1990 Vol. II [MWSYM]): 757-760.

This paper presents procedures, confirmed by both measured data and microwave circuit analysis, for designing dual-diode passive microwave limiters with low spike leakage (less than 100 nJ) and the ability to handle high peak power (on the order of 10 mJ of incident energy). Design parameters covered are diode characteristics, polarity, package parasitic, and the spacing between the diodes. Such limiters are needed to protect sensitive electronics from high-power rf.

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